



SiGe **ASIC** Developments for SQUID/TES Readout

D. Prêle, F. Voisin, C. Beillimaz, S. Chen, M. Piat, A. Goldwurm



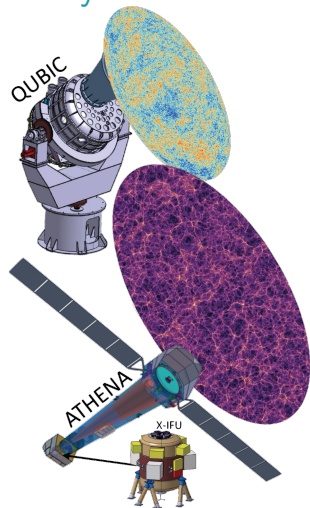
LTIC17

July 17-21, 2017
Kurume, Fukuoka, Japan



Introduction - on the use of large TES arrays

- ▶ Cosmic Microwave Background (CMB) observations - **QUBIC** ground based experiment - mm range
 - ▶ 2 k pixels TES focal plans (150 & 220 GHz)
 - ▶ **TDM** cryogenic readout with SQUIDs and LNA
 - ▶ Hot and Energetic Universe science theme - **X-IFU** instrument on-board of ATHENA space mission - X-ray range
 - ▶ 4 k pixels micro-calorimeter/TES focal plan
 - ▶ **FDM** cryogenic readout with LC and SQUID
- ⇒ *TES + Time or Frequency Domain Multiplexers*



Outline

SQUID/TES Readout

LNA design using BiCMOS SiGe technology

SQmux128, cryogenic ASIC for the TDM of QUBIC

AwaXe, Warm Front End Electronics for the FDM of ATHENA
X-IFU

Outlook



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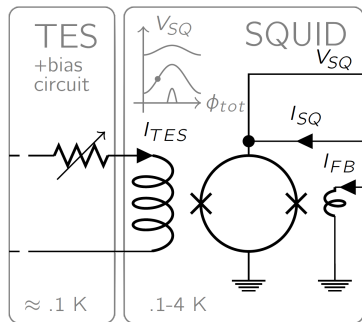
SQUID/TES Readout

- ▶ Voltage biased TES (for ETF) \Rightarrow **current readout**
- ▶ SQUID (Superconducting QUantum Interference Devices) + supercon. L_{in}
 \Rightarrow **cryogenic trans-impedance amplifiers** & multiplexer
- ▶ SQUID amplification and biasing I_{SQ} and offset adjustment



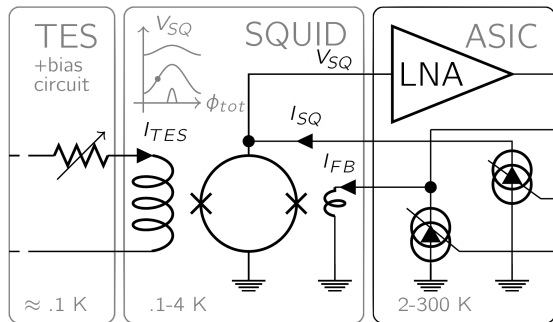
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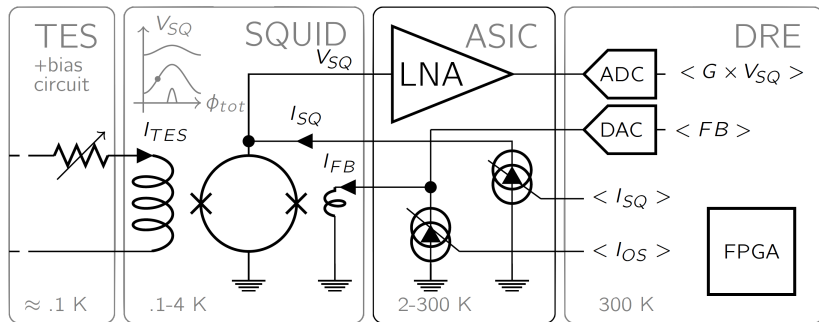
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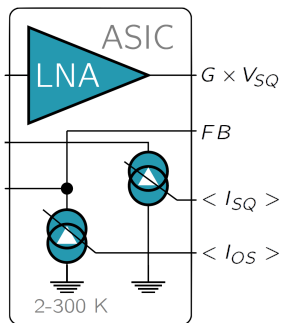
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LNA design using BiCMOS SiGe technology

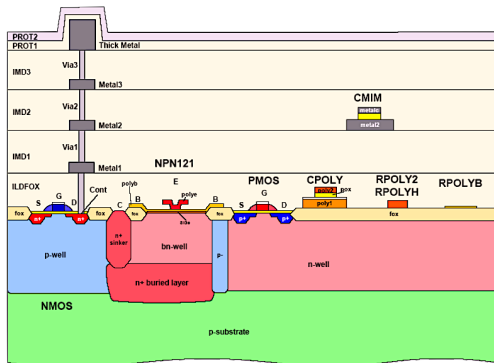
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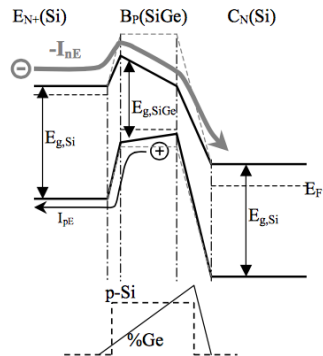
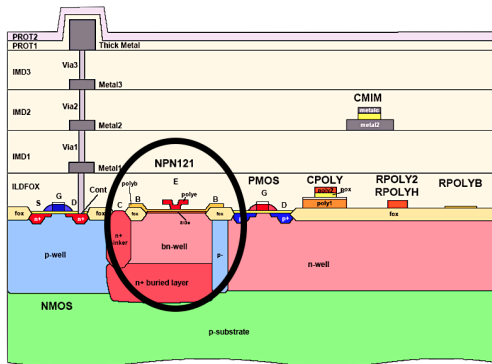
ASIC technology



AMS 0.35 BiCMOS S1Ge

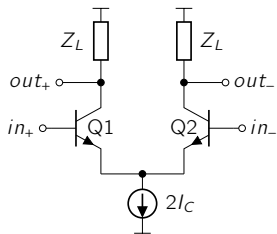


ASIC technology



Bipolar differential pair

Amplifier stage:



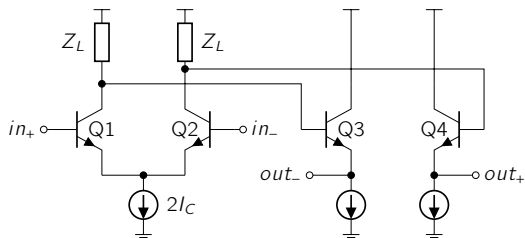
$$A_V \approx g_m \times Z_L \quad \text{and} \quad e_n \approx \sqrt{4qI_C}/g_m \quad \text{with} \quad g_m = \frac{\partial I_C}{\partial V_{BE}} = \frac{qI_C}{k_B T}$$

*Z_L could be simple resistances ($Z_L = R_L$)
or diodes ($Z_L \propto 1/g_m$)*

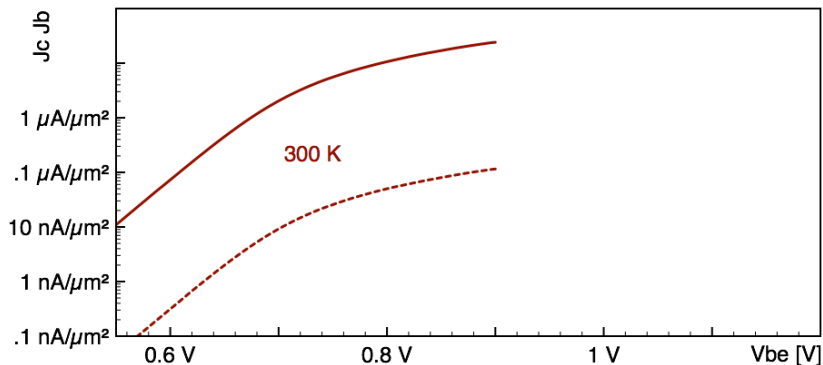


Bipolar differential pair

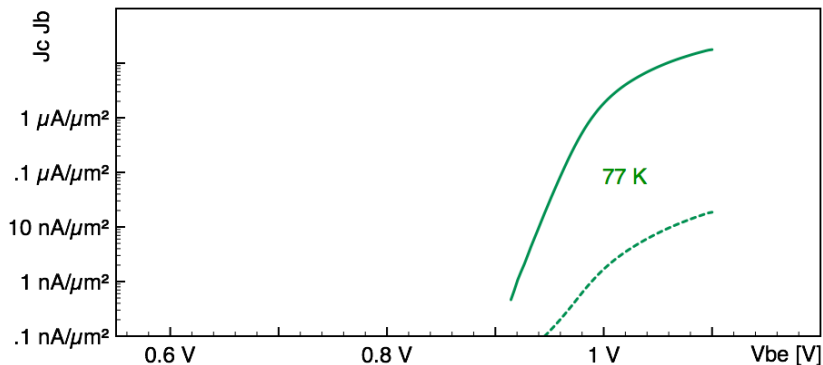
Amplifier stage with buffer stage:



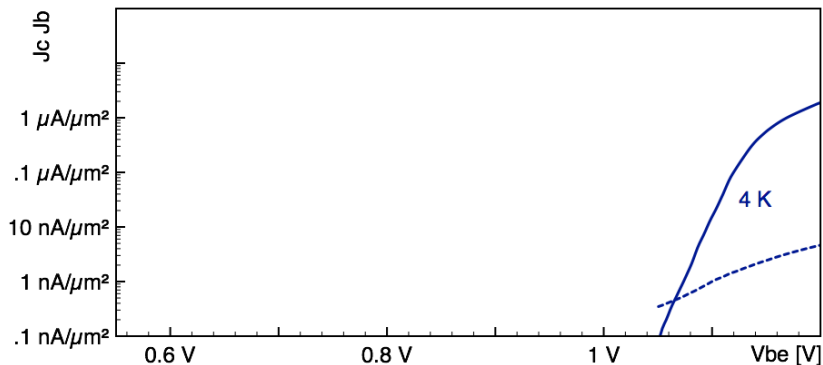
Sizing the bipolar transistor using Gummel Plot



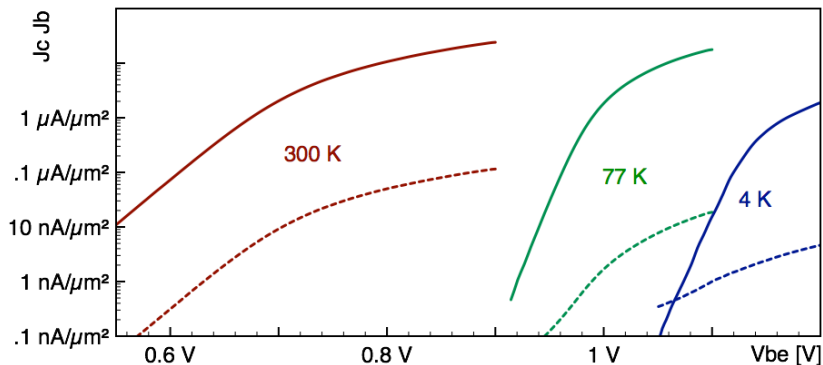
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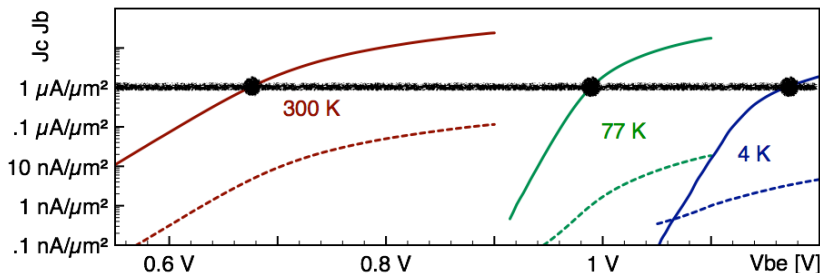
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Sizing the bipolar transistor using Gummel Plot



Sizing the bipolar transistor using Gummel Plot



*	300 K	77 K	4.2 K
$\beta = \frac{\partial I_C}{\partial I_B} \approx \frac{I_C}{I_B}$	180	1400	900
$g_m = \frac{\partial I_C}{\partial V_{BE}} \approx \frac{qI_C}{k_B T}$	30 mS	100 mS	150 mS
$h_{11} = \frac{\partial V_{BE}}{\partial I_B} = \frac{\beta}{g_m}$	6 k Ω	14 k Ω	6 k Ω

* Considering a HBT SiGe with $1000 \mu\text{m}^2$ area and $I_C = 1 \text{ mA}$;
 J_C is thus equal to $1 \mu\text{A}/\mu\text{m}^2$ ($1 \text{ mA}/1000 \mu\text{m}^2$)

$I_C = 1 \text{ mA} \Rightarrow$ Power consumption $> 10 \text{ mW}/\text{amplifier}$



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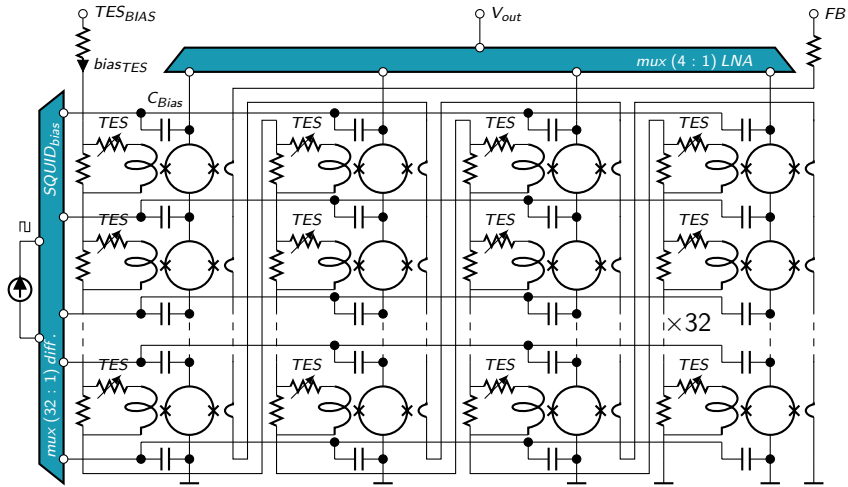
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QUBIC readout chain (simplified)

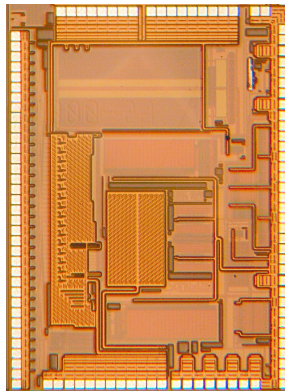
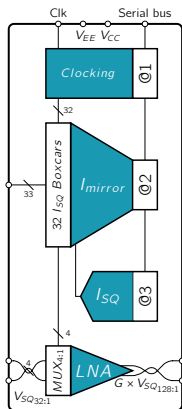


D. Prêle et al, "Capacitively-coupled SQUID Bias for Time Division Multiplexing", JLTP 2014

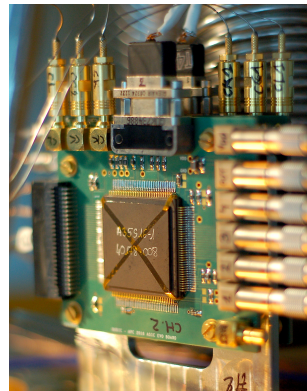
D. Prêle et al, "A 128 Multiplexing Factor Time-Domain SQUID Multiplexer", JLTP 2016



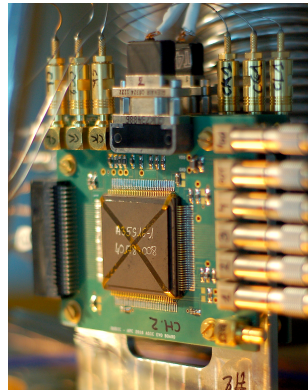
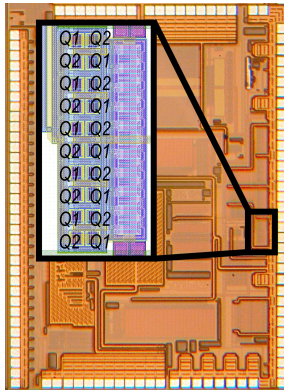
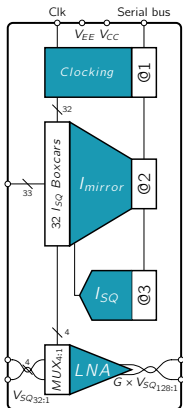
SQmux128 ASIC (77 K)



Chip size 2 mm × 4 mm



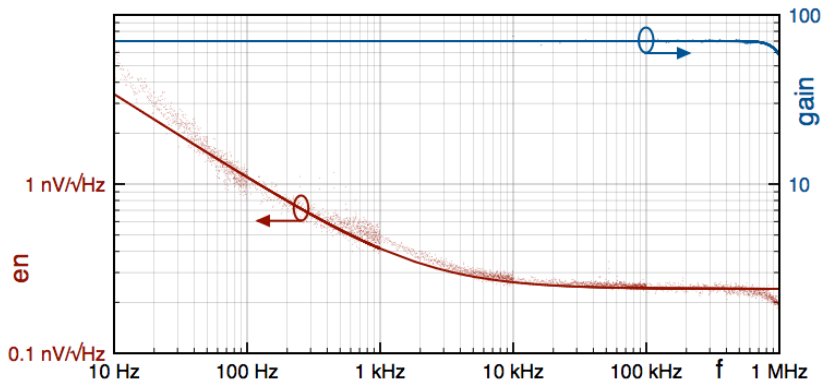
SQmux128 ASIC (77 K)



Q1 and Q2 input transistors are subdivided and mixed in a common centroid topology to well match the pair.



SQmux128 LNA performances at 77 K



* the cutoff frequency is due to a 1 MHz "antialiasing filter" in the QUBIC readout chain

** corner frequency increase at low temperatures is potentially due to increase sensitivity to thermal fluctuations of transistor parameters g_m , β , h_{11} (?)



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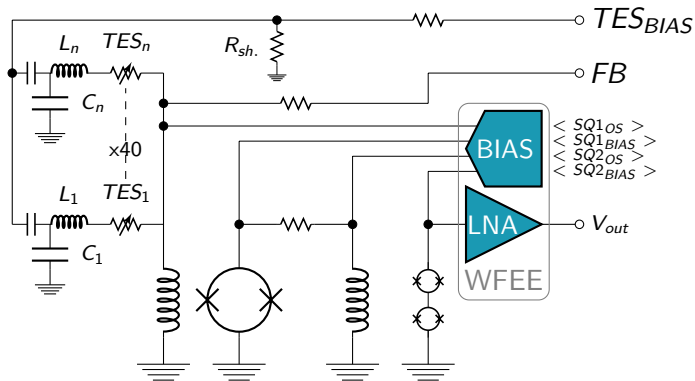
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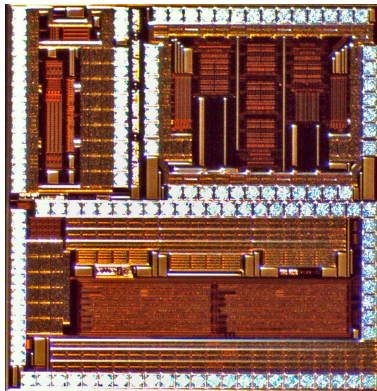
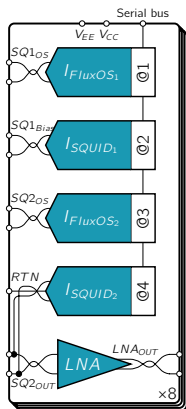
ATHENA X-IFU readout chain (simplified)



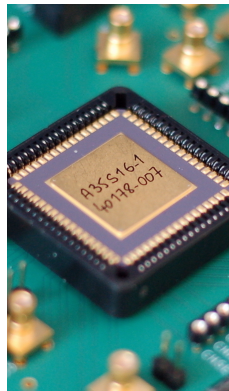
cryogenic temperature \longleftrightarrow room temperature



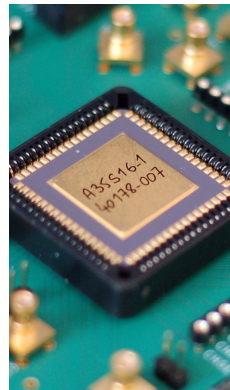
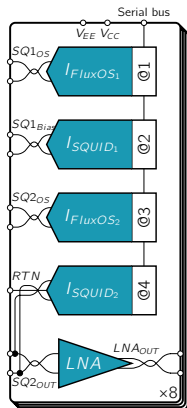
AwaXe ASIC (operated at 300 K)



Chip size 2.5 mm × 2.5 mm



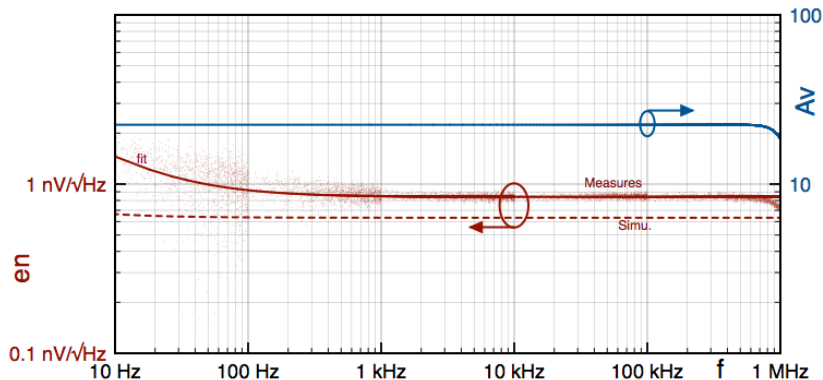
AwaXe ASIC (operated at 300 K)



D. Prêle et al, "Gain drift compensation with no feedback-loop developed for the X-Ray Integral Field Unit/ATHENA readout chain", JATIS 2016



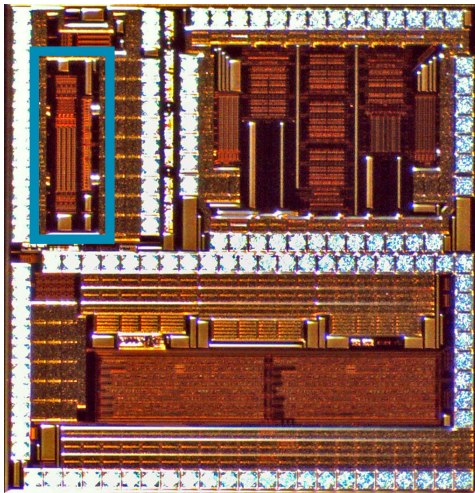
AwaXe LNA performances at 300 K



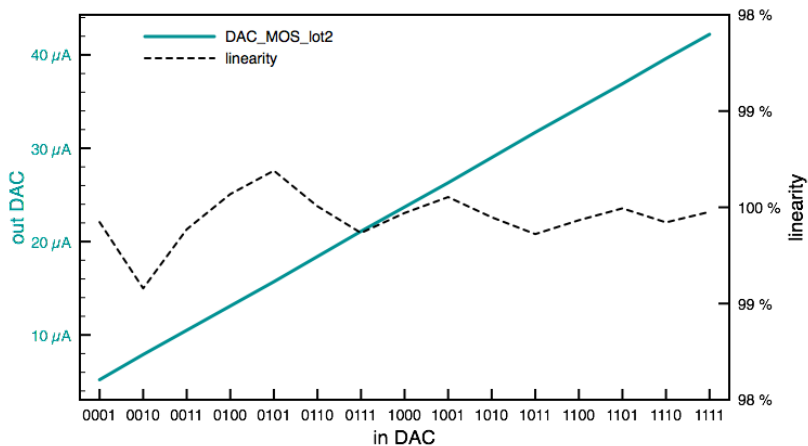
* the 1MHz cutoff frequency is due to the test set-up, the expected LNA bandwidth is > 20 MHz



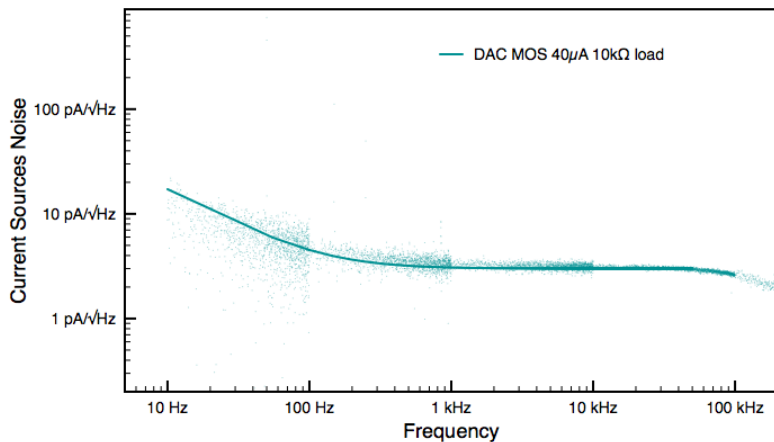
Current sources in the ASIC *AwaXe_v1*



Current sources linearity measurements



Current sources noise measurements



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- ▶ We are developing **ASIC** using **SiGe BiCMOS** technology for the readout of SQUID/TES devices.
- ▶ ASIC \Rightarrow **Small, lightweight and efficient**
 - ▶ **LNA**
 - ▶ **Biasing SQUID** (current DAC)
 - ▶ **serial decoder**
 - ▶ **sequencing** SQUID turning on for TDM

*So it is suitable for **whatever the multiplexing scheme and at whatever the needed temperature***

- ▶ *One is used in the **QUBIC** instrument (**TDM**)*
- ▶ *One is currently optimised for the **X-IFU** instruments (**FDM**)*

This last ASIC has been also "space qualified" up to 100 kRad TID and is Latchup free up to a LET > 120 MeV/mg/cm² (heavy ion ¹²⁴Xe³⁵⁺ tilt at 60°)

